

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Megahed, et al.

Application Serial No.: 09/930,747

Filed: August 14, 2001

Title: Structure And Method For

**Fabrication Of A Leadless Chip** 

Carrier With Embedded

Inductor

Group Art Unit: 2827

Examiner: CRUZ, L. C.

Notice of Allowance: August 14, 2003

## ISSUE FEE, PUBLICATION FEE AND FEE FOR FIVE COPIES OF THE ISSUED PATENT

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Enclosed, please find the completed "Issue Fee Transmittal" form and a "Payment by Credit Card" form (PTO-2038) in the amount of \$1,615.00 to cover the issue fee, publication fee, and fee for five (5) copies of the issued patent. The drawings filed on August 14, 2001 have been accepted as formal drawings by the Examiner.

Attorney Docket No.: 00CON159PC-CIP3

An early issuance of the present application is respectfully requested.

Respectfully Submitted; FARJAMI & FARJAMI LLP

Dated: 8/27/03

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